1 of 1

DOCKET NO.: 2003.10.004.WS0 **PATENT**
CLIENT NO.: SAMS01-00264
Customer No.: 23990

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	:	JOHN S. CSAPO ET AL.
U.S. Serial No.	:	10/696,502
Filed	:	October 29, 2003
For	:	SYSTEM AND METHOD FOR PROVIDING RELIABLE HARD HANDOFFS BETWEEN WIRELESS NETWORKS
Group No.	:	2681
Examiner	:	Not Yet Assigned

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Information Disclosure Statement;
3. Form PTO/SB/08B; and
4. Three (3) references

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to, MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 15, 2004.

Date: Sept 15, 2004 Kathy Hamilton
Mailer

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P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The publications herein are listed below and on the attached Forms PTO/SB/08B. Copies of the listed publications are submitted herewith.

Publications

Juan Ignacio Osa et al., "MOSFET-C Filter With On-Chip Tuning And Wide Programming Range", IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 10, October 2001.

Jose Silva-Martinez et al., "A 10.7-MHz 68-dB SNR CMOS Continuous-Time Filter With On-Chip Automatic Tuning", IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992.

Aydin Iiker Karsilayan, "A Novel Automatic Tuning Scheme For High-Frequency High-Q Continuous-Time Filters", Texas A&M University Department of Electrical Engineering, IEEE 2001.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: 8 Sept 2004

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/696,502
Filing Date	October 29, 2003
First Named Inventor	John S. Csapo
Art Unit	2681
Examiner Name	Not Yet Assigned
Attorney Docket Number	2003.10.004.WS0

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	AA	Juan Ignacio Osa et al., "MOSFET-C Filter With On-Chip Tuning And Wide Programming Range", IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 10, October 2001.	
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	AC	Aydin Iiker Karsilayan, "A Novel Automatic Tuning Scheme For High-Frequency High-Q Continuous-Time Filters", Texas A&M University Department of Electrical Engineering, IEEE 2001.	

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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